

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jae Jin Lee

Serial No.: 10/755,732

Filed: January 12, 2004

For: Signal Delay Control Circuit in a Semiconductor Memory Device

Group Art Unit: To be Assigned

Examiner: To be Assigned

I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service as First Class Mail, postage prepaid, on April 12, 2004, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450

Michael R. Jahl.

Reg. W. 33,902

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.97(b), enclosed herewith is a completed Form PTO-1449 identifying documents that may be considered material to the examination of the above-identified application.

The above-identified application is a continuation of and claims priority benefit under 35 U.S.C. § 120 to issued U.S. Patent No. 6,707,728 (issued March 16, 2004). Accordingly and pursuant to 37 C.F.R. § 1.98(d), copies of the documents identified on the accompanying Form PTO-1449 are not being enclosed as each was previously submitted by the applicants or cited by the examiner during prosecution of the '728 application.

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first office action on the merits.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 13-2855.

Respectfully submitted,

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April 12, 2004

By:

Michael R/Hull

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PTO-1449 (Modified)

U.S. Department of Commerce Patent and Trademark Office Atty. Docket No. Serial No. 30205/38071A 10/755,732
Applicant

## INFORMATION DISCLOSURE STATEMENT

Jae Jin Lee
Filing Date
January 12, 2004

Group
To be Assigned

U.S. PATENT DOCUMENTS						
*Examiner Initials	Document Number	Issue Date	Name	Class	Subclass	Filing Date if Appropriate
	5,263,000	11/16/1993	Van Buskirk et al.			
	5,473,198	12/05/1995	Hagiya et al.			
	5,548,560	08/20/1996	Stephens, Jr. et al.			
	5,757,705	05/26/1998	Manning			
	5,841,093	11/24/1998	Wada			
	5,933,379	08/03/1999	Park et al.			
	5,991,232	11/23/1999	Matsumura et al.			
-	6,055,209	04/25/2000	Abo			
	6,057,910	05/02/2000	Dunne			
	6,166,989	12/26/2000	Hamamoto et al.			
	2002/0190255A1	12/19/2002	Kitahara et al.			

FOREIGN PATENT DOCUMENTS								
*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation	
							Yes	No
	K	JP 6-350052	22.12.1994	Japan				
	X	JP 8-139287	31.05.1996	Japan				
	*	JP 11-306763	11.05.1999	Japan				
	X	JP 2000-21177	21.01.2000	Japan				

	O'	THER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
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EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.